

Project on Analysis, Design and Simulation of an S- Band MILO
DRDO Sponsored Project (No. GP/LT/EC/13-14/01)
DEPARTMENT OF ELECTRONICS ENGINEERING
INDIAN INSTITUTE OF TECHNOLOGY (BHU)
VARANASI – 221005

Draft Advertisement for putting on IIT (BHU) website

Applications are invited for one post of **Research Associates /Senior Research Fellow** in the Defense Research Development Organization, Govt. of India sponsored project entitled "**Analysis, Design and Simulation of an S- Band MILO**" for a period of one year. The post is purely temporary and co- terminated with the project. The required qualification, age and emoluments to be paid are as follows:

Essential

1. **Research Associate: Qualification:** Ph.D or M. Tech with three years experience with first class in the area of High Power Microwave Engineering.

Upper Age Limit: 35 years

Emoluments: Rs. 36,000/- P.M. Fixed + H.R.A. 20%

Candidates who has already completed their Ph.D work and thesis is under evaluation are also eligible to apply.

2. **Senior Research Fellow: Qualification:** 1) B.Tech / B.E. / M.Sc. or equivalent in Electronics Engineering with first class, 2) Two years experience in the area of Microwave Engineering and 3) NET / GATE or Equivalent

Upper Age Limit: 32 years

Emoluments: Rs. 28,000/- P.M. Fixed + HRA 20%.

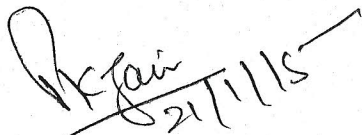
Desirable

The Candidates with experience and knowledge in the area of High Power Microwave Engineering and related field will be preferred and age relaxation upto duration of their experience in the area can be given.

A relaxation of five years in age will be given to SC / ST, Physically Handicapped, Female Candidates. All thing being equal, SC / ST candidates will be preferred as per GOI rules.

Application on plain paper giving Name, Permanent and Correspondence address, Name of Father and Mother, Telephone No. and emails, details of educational career along with self attested documents should reach to the **Professor P. K. Jain, Principal Investigator, D.R.D.O. Project, Department of Electronics Engineering, Indian Institute of Technology (BHU), Varanasi -221 005**, latest by 21st February, 2015.

No TA / DA will be paid to the candidates, if called for interview.



(P. K. Jain)

Principal Investigator,

D.R.D.O., Sponsored Project on S- Band MILO

Department of Electronics Engineering, IIT (BHU), Varanasi.