

Recruitment for Junior Research Assistant (JRA) Position

Applications are invited for the position of Junior Research Assistant (JRA) in a sponsored research project entitled "**Development of an AI-Enabled Platform for Remote Condition Monitoring of Overhead Power Transmission Line Insulators**", sponsored by the **Council of Science & Technology, U.P. (CSTUP)**, at **IIT (BHU), Varanasi** (Project ID No.: **2026RPMS00001611**). The project focuses on the design and development of AI-enabled sensing and monitoring systems for the remote condition assessment of overhead power transmission line insulators.

Name of the position	Junior Research Assistant (JRA)
Number of openings	1
Duration of the position	Initially for one-year; Extendable upto another two-year, on performance basis;
Emoluments	Rs. 28,000/- per month (Fixed Stipend/Fellowship/Salary)
Essential Educational Qualification Required	B.E. / B.Tech in Electrical Engineering, Electronics & Communication Engineering, Instrumentation Engineering, Computer Science Engineering, or allied branches, with a minimum of 60% marks (or equivalent grade) Only Indian citizens who are residents of Uttar Pradesh are eligible, as per CSTUP funding norms.
Desirable Qualifications	Valid GATE score in EE/EC/CS/AI (Artificial Intelligence & Machine Learning), or any other national-level eligibility examination in the relevant discipline
Upper age limit as on the last date of application	28 years Age requirements align with those set forth by the funding agency, with age relaxation applicable in accordance with Govt. of India regulations.
Preferred skill sets	Prospective candidates are strongly preferred to have hands-on experience in Artificial Intelligence (AI) and Machine Learning (ML) model development, signal and image processing, Python programming, sensor design, embedded systems, and high-voltage systems.
Nature of Duties	As part of the project, the prospective JRA will be required to: - Perform data acquisition in laboratory environment, including high-voltage systems and test setups - Design and implement AI/ML models for fault/condition detection using field-acquired data - Assist in data acquisition, system integration, field trials, and administrative matters regarding the project

Application Procedure (both steps are mandatory):

Step 1 – Email (Mandatory): Create a single PDF file containing the duly filled Application Form (attached at the end of this document) along with all relevant qualification certificates, marksheets, experience certificate, age proof, caste certificate, and other supporting documents. Mail the PDF file to the following email ID: **arupdas.eee@itbhu.ac.in** with the subject line "**Application for Project ID: 2026RPMS00001611**".

Step 2 – Google Form (Mandatory): In addition to the email submission, candidates must also fill out the Google Form through the link given below:

<https://docs.google.com/forms/d/e/1FAIpQLSdAqc3Zpocthj4sVN5MyAmZ5RgPTKQBCPZ0KMYwfrSWtcOMNw/viewform?usp=publish-editor>

Note: No hard copy is required. Applicants need to show all the original documents at the time of the interview, if shortlisted, based on the data submitted in the application.

For any queries, candidates may contact:

Dr. Arup Kumar Das,
Asst. Professor, Dept. of Electrical Engg.,
IIT (BHU), Varanasi
Email: **arupdas.eee@itbhu.ac.in**

Please note that your Google Form submission must clearly mention all the details below:

- i) Name & Age, Date of Birth.
- ii) Contact details including Email ID, phone number, and contact address.
- iii) Educational qualifications including percentage of marks, college & university of graduation, year of graduation.
- iv) GATE/NET score (if any) with year of passing, All India Rank.
- v) Previous work experience and relevant details, if any.
- vi) Significant technical achievements and projects worked on earlier.

Applications with incomplete information will not be considered during the shortlisting process. Only shortlisted candidates will be intimated through email regarding the date of the interview.

Deadline for submission of Applications: [21/09/2026, 23:59:59 IST]

General Terms and Conditions:

- 1) The position is purely temporary and co-terminus with the project. The candidate is expected to join immediately if selected.
- 2) Only shortlisted candidates will be communicated to appear in the interview, and no other communications in this regard will be entertained. No TA/DA will be paid for attending the interview.
- 3) The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
- 4) Other relevant Guidelines of the funding agency and the GoI rules will be followed.

Arup Kumar Das
सहायक आचार्य / ASSTT. PROFESSOR
विद्युतीय अभियंत्रिकी विभाग / Department of Electrical Engineering
भारतीय प्रौद्योगिकी संस्थान / Indian Institute of Technology
(कश्मीर हिन्दू विश्वविद्यालय) / (Banaras Hindu University)
Varanasi, U.P. (INDIA)

Dr. Arup Kumar Das
Principal Investigator &
Asst. Professor,
Dept. of Electrical Engg.,
IIT (BHU), Varanasi

Application Form

Personal details:			
Name			
Father's name			
Mother's name			
E-mail		Phone No.	
Date of Birth		Category*	GN / OBC / SC / ST
Permanent Address		Correspondence Address	

**Certificate to be attached if OBC/SC/ST.*

Education Qualifications:

Qualification	University/Board	Year of passing	Percentage/CGPA	Subject
10th				
12th				
B.Tech./B.E./B.Sc.				
M.Sc.				
Others				

Details of qualifying examination (GATE, NET, or Others):

Name of Examination	Subject of Examination	Year of Passing	Score	Marks Obtained

Details of Professional / Work Experience*:

Organization	Designation	Start Date	End Date

**At least the current employment should be mentioned.*

The details furnished in this form are correct to my knowledge and belief.

Name and Signature

Date:

Place: