



DEPARTMENT OF ELECTRONICS ENGINEERING  
INDIAN INSTITUTE OF TECHNOLOGY  
(BANARAS HINDU UNIVERSITY)  
VARANASI-221005

**Dr. Kishor P. Sarawadekar**  
Associate Professor

☎: +91-98387 64487

Email: [skishor.ece@iitbhu.ac.in](mailto:skishor.ece@iitbhu.ac.in)

July 11, 2025

### **Recruitment of Internship Students in Department of Electronics Engineering**

Applications are invited from the UGD/IDD final or pre-final year students for the internship position in the research project entitled “**Real Time Classification of Aerial Targets such as Drone, Bird etc. on FPGA Boards**” sponsored by the **DRDO Young Scientist Lab – Cognitive Technologies (DYSL-CT)** awarded to **Dr. Kishor Sarawadekar**.

Name of the Position	<b>Internship (Hardware Profile)</b>
Number of Positions	Three (3)
Work Profile	In this project, selected candidates will be working on: 1) Design and development of algorithms for real time classification of aerial targets. 2) Implement the proposed low power design on FPGA board. 3) Conduct Field trials and perform other assigned tasks 4) Compiling data, writing manuscripts/reports and presenting data in conferences
Desirable	The applicant must possess knowledge of digital image processing and concepts of AI/ML. It is desirable that s/he is well versed with High Level Synthesis and HDL coding. Knowledge of hardware boards and peripheral interfacing concepts is expected.
Name of the Position	<b>Internship (Software Profile)</b>
Number of Positions	Three (3)
Work Profile	The applicant must have good knowledge on the fundamental concepts of Machine/Deep Learning along with strong implementation skills in C++ and Python. S/he must also have prior experience in developing, training, and validating Deep Learning models.
Duration of both Positions	One year but extendable up to eighteen months.

### **General Terms and Conditions:**

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of resume.

3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately, if selected.

Interested candidates should send their applications strictly in the attached application format via email to **Principal Investigator, Dr. Kishor Sarawadekar** at [skishor.ece@iitbhu.ac.in](mailto:skishor.ece@iitbhu.ac.in) on or before **4<sup>th</sup> August, 2025**. Please write “**Application for Internship ..... profile**” in the subject line of the email. The shortlisted candidates will be intimated by email.

**Application Instructions:**

- Mention “Application for Internship .... profile” in the subject line of the email.
- Canvassing in any form will lead to disqualification of candidature.
- Venue of Interview – Department of Electronics Engineering, IIT (BHU), Varanasi-221005.  
Contact no: +91-98387 64487
- Application should be according to the attached format. In addition, you may attach your detailed resume.
- Last date for receiving completed application is **4<sup>th</sup> August, 2025**.
- Students must carry their identity cards on the day of the interview.

## **Application for Internship Position**

**Name of the Profile Applied for .....**

1. Full Name: PHOTO
2. Date of Birth:
3. Gender (M/F):
4. Category (GEN/SC/ST/OBC):
5. Nationality:
6. Physically handicapped: Y/N
7. Father's Name:
8. Address of Correspondence:
  - a. Phone No.:
  - b. Mobile No.:
  - c. E-mail Address:
9. Other Details:
  - a. Details of the project carried out with duration, place of work, area worked on, mentor/guide's name, description of the project and contribution to the project (in not more than 5 sentences)
  - b. Publications (if any):

### **Declaration**

I hereby declare that I have carefully read and understood the instructions and particulars on this application and that all entries in this form as well as in the attached sheets are true to the best of my knowledge and belief.

Date:  
Place:

Signature