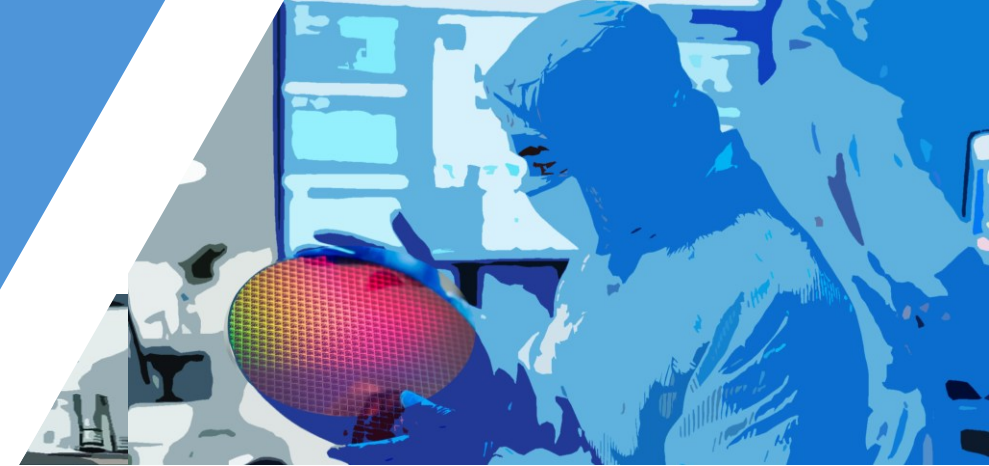


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Semiconductor & Electronics Ecosystem

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Detailed Workshop Schedule

	10:00 – 11:30 01	12:00 – 13:30 02	15:00 – 16:30 03	17:00 – 18:30 04
DAY - 1	Young Minds, Big Impact: Shaping India's Semiconductor Revolution Sri Ashok Chandak, President SEMI India & IESA	Trends in VLSI Industry Sri Shubham Paliwal, Engg. Leader, Siliconch Systems (L&T SCT)	Introduction to Semiconductor Technology Dr. Ankit Arora, Asst. Prof. ECE, IIT (BHU) Varanasi	VLSI Design – I : HDL Coding for Digital Circuits Design Dr. K P. Sarawadekar, Asso. Prof. ECE, IIT (BHU) Varanasi
DAY - 2	More Than Moore – Insights into Technology, Modeling & Benchmarking Dr. Naveen Karumuri, MTS Device Modeling, Global Foundries.	VLSI Design – II : Logic Synthesis and Equivalence Checking Dr. K P. Sarawadekar, Asso. Prof. ECE, IIT (BHU) Varanasi	VLSI Design – III : Algorithm to RTL Design Dr. K P. Sarawadekar, Asso. Prof. ECE, IIT (BHU) Varanasi	Photolithography: Precision Patterning in Semiconductor Fabrication Dr. Ankit Arora, Asst. Prof. ECE, IIT (BHU) Varanasi
DAY - 3	22-nm Planar FET Fabrication Process Flow – I Dr. Ankit Arora, Asst. Prof. ECE, IIT (BHU) Varanasi	22-nm Planar FET Fabrication Process Flow – II Dr. Ankit Arora, Asst. Prof. ECE, IIT (BHU) Varanasi	From Node to PDK: Translating Foundry Collaterals into Design-ready Kits Sri Shankar C. J., Systems & H/w Enabling Engineer, Intel Corporation	Device to System Level Interaction: GAN MMIC Design Dr. Jaya Jha, Asst. Prof. ECE, IIT (BHU) Varanasi
DAY - 4	Integration Challenges: From Failure Analysis to Yield Enhancement Dr. Praveen Sahu, Sr. Process Engineer, Jerih Semiconductor Inc.	Solution Processing in Electronics Dr. Shivam Verma, Asst. Prof. ECE, IIT (BHU) Varanasi	Semiconductor Inspection and Metrology – Imaging Techniques Dr. Anchal Srivastava, Professor Physics, Institute of Science, BHU	Chiplets and the Future of IC Packaging Sri Mithun C. A., Process Integration Engineer, TATA Electronics
DAY - 5	Reliability Investigations and Mechanisms in Semiconductors Dr. Jaya Jha, Asst. Prof. ECE, IIT (BHU) Varanasi	Towards Next-Gen FET Scaling: An Integration & Device Perspective Dr. Ritam Sarkar, Research Engineer (Compute), IMEC-Leuven	Semiconductor Inspection and Metrology – Spectroscopic Techniques Dr. Anchal Srivastava, Professor Physics, Institute of Science, BHU	Advances in Semiconductor Patterning Techniques Dr. Ankit Arora, Asst. Prof. ECE, IIT (BHU) Varanasi