



भारतीय
प्रौद्योगिकी
संस्थान
काशी हिन्दू विश्वविद्यालय



INDIAN
INSTITUTE OF
TECHNOLOGY
BANARAS HINDU UNIVERSITY

**Recruitment of Senior Research Assistant in the
Department of Electronics Engineering, IIT (BHU)**

Project Code No (Institute): R&D/ECE/CSTUP/24-25/21/temp
CST UP Code: CST/E&T/D-686

Applications are invited from residents of Uttar Pradesh for the following position for the research project titled "Fabrication and Characterization of Hybrid Carbon-based Nanomaterial-Polymer Thin Film Transistors for Poisonous Gas Sensing Application (Project ID: 3662)" sponsored by Council of Science and Technology (CST), Government of Uttar Pradesh (UP).

1. **Name of the position:** Senior Research Assistant (SRA)
2. **Number of positions:** 1
3. **Last date of Application:** Applicants should apply within 21 days of this advertisement.
4. **Emoluments (p.m.):** Rs. 28000/- for three years
5. **Essential Educational qualification:**
 - i. B.E/B.Tech in Electronics and Communication Engineering with First Class and two years of research experience
OR
 - ii. M.E./M.Tech. with First Class in Electronics and Communication/allied disciplines

6. Desirable qualification:

Knowledge of Thin film device fabrication and characterization.
Knowledge of programming in c/c++, python and scripting languages like perl, and tcl/tk.

Age: maximum 32 years on 01-04-2024

Age Relaxation: As per R & D guidelines of CST UP

Application Procedure:

Make a single PDF file having the filled application form (attached) with all relevant qualification certificates, marksheets, experience certificate, age proof, caste certificate and others. Mail the PDF file to the following mail id "srg.iitbhu.2019@gmail.com" and CC to

“shivam.ece@iitbhu.ac.in” with subject line “Application for the position of SRA in CST UP Sponsored Project”.

Note: No hard copy is required. Applicant needs to show all the original documents at the time of the interview/online interview, if shortlisted, based on submitted data in the application.

General Terms and Conditions:

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately, if selected.
5. No TA/DA will be paid for attending the interview.
6. Other relevant CST UP Guidelines and UP Gov. rules will be followed.

Please feel free to contact me in case of any query:



Dr. Shivam Verma (Principal Investigator),
Assistant Professor,
Department of Electronics Engineering,
Indian Institute of Technology (BHU)
Varanasi 221005 (U.P.) INDIA
Email ID: shivam.ece@iitbhu.ac.in

Application Form

Personal details:

Name	
Father's name	
Mother's name	
E-mail	
Phone No.	
Date of Birth and Category (GN/OBC/SC/ST/)	(certificate to be attached if OBC/SC/ST)
Permanent Address	
Correspondence Address	

Details of Education Qualification

Qualification	University/Board	Year of passing	Percentage /CGPA	Subject
10th				
12th				
B.Tech./B.E.				
M. Tech.				
Others				

Details of examination qualified if any (GATE, Net, or Others)

Name of Examination	Subject of Examination	Year of Passing	Score	Marks Obtained

Details of Professional / Work Experience: At least current employment should be mentioned

Organization	Designation	Date-From	Date-To

The details furnished in this form are correct to the best of my knowledge and belief.

Signature with Date and Name