

Advertisement for Research Associate/Project Engineer/Project assistant

Department of Electrical Engineering

IIT (BHU) Varanasi

Sponsored Project no. R&D/SA/DRDO/EE/24-25/01/555

Applications are invited from Indian nationals for the following positions in the research project entitled “**Compact Pulse power supply using sequentially controlled PFN based on Silicon carbide high gain modular converter**” sponsored by Defence Research and Development Organisation (DRDO), Government of India.

1. Research Associate

Name of Position	Research Associate (RA)
Number of Positions	02
Duration of the Position	Three years. Initially the appointment will be made for one year and extendable up to three years on performance basis
Emoluments	RA-II: Rs. 61000/- (HRA @18% will be additional) RA-I: Rs. 58000/- (HRA @18% will be additional)
Educational Qualification	PhD in Electrical Engineering or any allied branches with at least one paper in SCI journal OR ME/MTech in Electrical Engineering or any allied branches with 3 years of research, teaching, and design and development experience & at least one paper in SCI journal
Desirable Experience	Experience in the area of Pulsed power supply, Power electronics, and related field
Age Limit	Upper age limit – 35 years. Age relaxation applicable as per Government of India (GoI) rules

2. Project Engineer

Name of Position	Project Engineer
Number of Positions	01
Duration of the Position	Three years. Initially the appointment will be made for one year and extendable up to three years on performance basis
Emoluments	Rs. 58000/- (HRA @18% will be additional)
Educational Qualification	PhD in Electrical Engineering or any allied branches with at least 1 year of Industrial experience OR ME/MTech in Electrical Engineering or any allied branches with at least 3 years of Industrial experience OR Bachelor Degree in Engineering Electrical Engineering or any allied branches with at least 6 years of Industrial experience
Desirable Experience	Experience in the area of Pulsed power supply, Power electronics, and related field
Age Limit	Upper age limit – 35 years. Age relaxation applicable as per Government of India (GoI) rules

3. Project Assistant

Name of Position	Project Assistant
Number of Positions	01
Duration of the Position	Three years. Initially the appointment will be made for one year and extendable up to three years on performance basis
Emoluments	Rs. 20000/- (HRA @18% will be additional)
Educational Qualification	Any basic degree in Science/Arts/Professional courses with Computer skill and relevant work experience
Age Limit	Upper age limit – 50 years. Age relaxation applicable as per Government of India (Gol) rules

Application should be strictly according to the attached format. Applicants can send their application along with self-attested copies of certificates, documents by Post/Email **on or before 17th June 2024** to the principal Investigator

Dr. Santosh K Singh
Professor
Department of Electrical Engineering
IIT (BHU) Varanasi
UP-221005, India
Email: sksingh.eee@iitbhu.ac.in

General terms and conditions:

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be communicated to appear for the interview and no other communication in this regard will be entertained.
4. Original certificates and self-attested copies of all certificates need to be presented for verification.
5. No TA/DA will be paid for attending interview

APPLICATION FORM

1. Full Name:
2. Date of Birth:
3. Sex (M/F):
4. Category (GEN/SC/ST/OBC/EWS):
5. Marital Status:
6. Nationality:
7. Physically handicapped: Yes/No
8. Father's Name:
9. Mother's Name:
10. Address of Correspondence:
 - a. Mobile/Tel. No.:
 - b. E-mail Address:
11. Permanent address:
12. Academic Record:

PHOTO

- a. Educational qualification:

Examination passed	Name of School/ Institute / University	Year of passing	Subjects	Percentage of marks/GPA obtained /Division
10 th				
10+2 Sc				
Bachelor's degree				
Master's degree				
PhD degree				

- b. GATE Qualification details:

GATE	Year:	Qualified: Yes/No	Score:

- c. Details of the project carried out with project title, place of work
- d. Attach list of publications (if any):
- e. Working experience (if any):
- f. Whether currently employed: Y/N
- g. Time required to join this position:

13. Names and addresses of two referees along with phone number and e-mail address.

Declaration

I hereby declare that I have carefully read and understood the instructions and particulars for this application and that all entries in this form as well as in the attached sheets are true to the best of my knowledge and belief.

Date:

Place:

Signature