

### Recruitment for JRF Position

Applications are invited from motivated and eligible candidates for the position of Junior Research Fellow (JRF) in a research project entitled “**Machine-Learning Enabled Next-Generation IoT Communications**” sponsored by DST-INRIA, Indo-French Centre for Promotion of Advanced Research (IFCPAR/CEFIPRA). Project focuses on 6G and beyond wireless network design and analysis. Selected JRF will be involved in the design and development of signal processing, machine learning and communication part of the proposed project.

Name of the Position	Junior Research Fellow (JRF)
Number of Position(s)	One (1)
Duration of the Position	One year. Extendable on performance basis Selected candidate may be considered for admission to Ph.D. Programme at IIT (BHU) Varanasi.
Emoluments	Rs. 37,000 + HRA/ Month
Educational Qualification	(i) B.E./B.Tech., with minimum of 60% marks in Electronics and Communication Engineering and having GATE score. (or) (ii) M.E./M.Tech., in Electronics and Communication Engineering/Signal Processing with minimum of 60% marks (iii) Experience in the area of signal processing, machine learning, wireless communication and related field is desirable.
Upper Age limit as on the last date of application	Age requirements align with those set forth by the funding agency, with age relaxation available in accordance with Government of India regulations.

#### General Terms and Conditions:

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately, if selected.
5. No TA/DA will be paid for attending the interview

Interested candidates are required to submit a detailed CV to Dr. Sanjeev Sharma, Department of Electronics Engineering, Indian Institute of Technology (BHU) Varanasi, via e-mail: [sanjeev.ece@iitbhu.ac.in](mailto:sanjeev.ece@iitbhu.ac.in). Please note that your CV should include contact details (address, mobile phone no., email ID), date of birth, qualifications clearly mentioned, Category. Complete information with regards to NET/GATE score such as: year of passing and validity, discipline, score, marks, all India Rank and number of candidates appeared should be mentioned in the CV. Incomplete applications will be rejected. Last Date for submission of applications is **July 20, 2024**. Only shortlisted candidates will be intimated by email for the interview.



18/6/24