



इलेक्ट्रॉनिकी अभियांत्रिकी विभाग
भारतीय प्रौद्योगिकी संस्थान
(काशी हिन्दू विश्वविद्यालय)
वाराणसी-२२१००५
DEPARTMENT OF ELECTRONICS ENGINEERING
INDIAN INSTITUTE OF TECHNOLOGY
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Prof. Satyabrata Jit

Date: July 24, 2024

Professor (HAG)

Advertisement for the Junior Research Fellow (JRF)

Applications are invited from Indian nationals for the following position in the research project entitled “**Design and Development of Composition Engineered Toxic-Free Organic-Inorganic Perovskite Quantum Dots Based Flexible Spectrum-Tunable Photodetectors**” sponsored by the *Science and Engineering Research Board (SERB)*, Department of Science and Technology, Government of India, New Delhi.

Name of the Position	Junior Research Fellow (JRF)
Number of Position(s)	One (01)
Duration of the Position	Six months or 04.03.2025 whichever is earlier.
Emoluments	Rs. 37,000 + HRA @ 16 % per month
Educational Qualification & Experience	(i) B.E./B.Tech., with a minimum of 55% marks (or equivalent grade) in Electronics and Communication Engineering and having a valid GATE score. (or) (ii) M.E./M.Tech in Electronics Engineering with minimum of 55% marks or equivalent grade and GATE qualified. (iii) Candidates with expertise in the area of microelectronics/nanoelectronics/flexible electronics/optoelectronics/conducting polymer electronics will be preferred.
Age	Should not exceed 28 years as on the last date of the application. Age relaxation may be applicable as per Govt. of India norms.
Last date of receiving application	August 15, 2024

General Terms and Conditions:

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately, if selected.
5. No TA/DA will be paid for attending the interview.

Interested candidates may send their soft copy of the application at sjit.ece@iitbhu.ac.in on or before August 15, 2024. Candidates need to mention their name, parents' name, date of birth, permanent and correspondence addresses, contact number and e-mail address, details of educational qualifications (starting from the High School or equivalent), specialization and experiences (if any) and any other details along with self-attested copies of the certificates, mark sheets, GATE qualifying certificate and other testimonials (if any).

(Prof. S. Jit)

प्राचार्य / Professor
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