



**DEPARTMENT OF ELECTRONICS ENGINEERING
INDIAN INSTITUTE OF TECHNOLOGY
(BANARAS HINDU UNIVERSITY)
VARANASI-221005**

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Recruitment of Junior Research Fellow in Department of Electronics Engineering, at IIT (BHU)

Applications are invited from Indian nationals for the following position in the research project entitled “**Development of Variable data rate CCSDS compliant Direct Digital Demodulator**” sponsored by the **Indian Space Research Organization’s SAC** awarded to **Dr. Kishor Sarawadkar**.

Name of the Position	Junior Research Fellow (JRF)
Number of Position(s)	One (1)
Duration of the Position	One year. Extendable up to two years (2 Years) based on the performance.
Emoluments	Rs.31,000 + HRA (as applicable) per month
Educational Qualification	1) B.E./B.Tech., with minimum of 55% marks in Electronics and any relevant Engineering discipline with a valid GATE score. (or) 2) M.E./M. Tech., in Electronics and any relevant Engineering discipline (but preferably in Digital/ VLSI/ Communication/ Signal Processing Engineering) with minimum of 55% marks and GATE qualified. 3) Experience in the area of Digital Signal Processing, Digital Communication Systems, FPGA Based System Design and related field is desirable.
Job Profile	In this project, selected candidate will be working on: 1) Development of efficient algorithms for modulation and demodulation techniques 2) HDL coding, implementation of algorithms on FPGA or a suitable hardware platform and other assigned tasks 3) Compiling data, writing manuscripts/reports and presenting data in conferences
Upper Age limit as on the last date of application	28 years. Age relaxation applicable as per Government of India (GoI) rules.

General Terms and Conditions:

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately, if selected.
5. No TA/DA will be paid for attending the interview.

Interested candidates should send their applications strictly in the attached application format via email to **Principal Investigator, Dr. Kishor Sarawadkar** at skishor.ece@iitbhu.ac.in on or before **24th March, 2022**. Please write “**Application for JRF**” in the subject line of the email. The shortlisted candidates will be intimated by email. Tentative date of interview: within two weeks from the application deadline.

Application Instructions:

- Original certificates and self-attested copies of all certificates need to be presented before the interview for verification.
- Canvassing in any form will lead to disqualification of candidature.

- Venue of Interview – Department of Electronics Engineering, IIT (B.H.U.), Varanasi-221005. Contact no: +91-98387 64487
- Application should be strictly according to the attached format.
- Last date for receiving completed application is **24th March, 2023**.

NOTE: The selected candidate can be considered for PhD program after fulfilling the required criteria of IIT (BHU).

APPLICATION FOR JRF (Project) POSITION

1. Full Name: PHOTO
2. Date of Birth:
3. Gender (M/F):
4. Category (GEN/SC/ST/OBC):
5. Marital Status:
6. Nationality:
7. Physically handicapped: Y/N
8. Father's Name:
9. Address of Correspondence:
 - a. Phone No.:
 - b. Mobile No.:
 - c. E-mail Address:
10. Academic Record:
 - a. GPAT/GATE/CSIR/UGC National Exam Qualification details with Percent marks/Rank, year of qualifying etc.
 - b. Qualifying Degree:

Examination with Specialization	Name of School, College, University/Institute, with city and state name	Year of passing	Subjects	Percentage of marks/GPA obtained	Distinction/Division

- c. Details of the project carried out with duration, place of work, area worked on, mentor/guide's name, description of the project and contribution to the project (in not more than 5 sentences)
 - d. Attach list of publications (National and International separate) with a clear mention of publication year, impact factor (if any):
 - e. Working experience (if any):
 - f. Whether currently employed: Y/N
 - g. Time required to join this position
11. Names and addresses of three referees along with phone number and e-mail address.

Declaration

I hereby declare that I have carefully read and understood the instructions and particulars on this application and that all entries in this form as well as in the attached sheets are true to the best of my knowledge and belief.

Date:
Place:

Signature