

## **Advertisement for the post of Project Associate under the SMDP Project of the DeitY, Govt. of India**

Applications are invited in the plain paper for the following position under the SMDP-C2SD project of the DeitY, Govt. of India at the Indian Institute of Technology (BHU), Varanasi.

**Name of the Post:** *Project Associate*

**Number of the posts:** 01 (One)

**Minimum Qualifications and Experience:** Candidate should have first class or equivalent in B.Tech./B.E. degree in the Electronics and Communication Engineering or equivalent or MCA from a reputed institutions/university. Candidate must have knowledge in maintaining the servers running on both the windows and Linux OS environments. Preference will be given if the candidate has knowledge in working with VLSI related software.

**Job Responsibilities:** The selected candidate must be capable of doing the following:

- To maintain/update the servers/workstations/PCs running various VLSI software in the Windows and Unix OS environments.
- To help the undergraduate students in the implementation of their projects in the area of VLSI/SoC
- To prepare various documentation related to the projects
- Any other works related to the project to be assigned from time to time.

### **General Terms and Conditions:**

**Duration of the Project:** Appointment will be made on purely contractual basis initially for a period of ONE YEAR with a fixed salary of **Rs. 20,000/-** (Rupees Twenty Thousand only) per month. However, depending upon the satisfactory performance of the candidate, his/her appointment may be extended for the maximum period of 4 years or upto the end of the project fund whichever is earlier with a possible annual increment of 10% depending on the availability of funds.

**Mandatory Information to be Provided in the Application:** Name of the Candidate; Father's Name; Mother's Name; Gender; Date of Birth; Contact Details with full address for correspondence, mobile number, email etc.; Details of the Academic Qualifications; Technical skills in the relevant area of VLSI, if any; Working experience, if any; and any other relevant Information.

**Selection Process:** Interested candidates are requested to send their application to **Prof. S. Jit, Chief Investigator, SMDP-C2SD Project, Department of Electronics Engineering, IIT(BHU), Varanasi-221005** by speed post/regular post with a softcopy at [sjit.ece@iitbhu.ac.in](mailto:sjit.ece@iitbhu.ac.in) latest by **June 24, 2017**. The short-listed candidates will be intimated to appear for written test and/or Interview. They are required to bring with all the original documents relevant to their academic qualifications/experience etc. No TA/DA as well as accommodation will be provided for attending the Interview. The institute reserves the right to fill or not to fill the post without any justification.